

Notice of References Cited	Application/Control No. 10/552,805		Applicant(s)/Patent Under Reexamination BRIL ET AL.	
	Examiner DOUGLAS BRYANT		Art Unit 4123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,587,928	12-1996	Jones et al.	709/204
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Streit, A: "On the Job scheduling for HPC-Clusters and the dynP Scheduler" The Institution of Electrical Engineers, December 2001 Section 5 "The dynP Scheduler" pages 62-63			
	V	NARLIKAR G JET AL: "SPACE-EFFICIENT IMPLEMENTATION OF NESTED PARALLELISM" ACM SIGPLAN NOTICES, ASSOCIATION FOR COMPUTING MACHINERY, NEW YORK, US, vol. 32, no. 7, 1 July 1997 Pages 25-36			
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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